HPC workshop on heterogeneous architectures with SYCL / INTEL ONEAPI

lundi 28 novembre 2022 - jeudi 1 décembre 2022

Ecole Polytechnique - Aile 0 - Ground Floor

Programme

Day 1 - Monday 28/11
Morning session
General public introduction : heterogeneous programming
Afternoon session
Program the heterogeneous environment without constructor dependency with SYCL with examples of the code
Day 2 - Tuesday 29/11
Morning session
Presentation of oneDPL, oneTBB, oneMKL calculation libraries
Compatibility Tool (CUDA/SYCL conversion)
Afternoon session
Tutorial on using the Compatibility Tool
Day 3 - Wednesday 30/11
Morning session
Profiling and analyzing code performance using Vtune/Advisor
Afternoon session
Practical work code optimization
Day 4 - Thursday 01/12
Morning session
Review of Intel Tools for Artificial Intelligence
Afternoon session
AI practical work